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PTO/SB/81 (04-05)
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03-0730

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number **Application Number** 10/559,134 Filing Date September 10, 2003 **POWER OF ATTORNEY** First Named Inventor Mirabedini, Mohammed R. and Title Apparatus and Method of.. CORRESPONDENCE ADDRESS Art Unit 2823 INDICATION FORM **Examiner Name** Coleman, William D.

		Attorney Docket Nu	mber 03-07	/30		
I hereby revoke all previous powers of attorney given in the above-identified application.						
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1 am the:						
Applicant/Inventor.  Assignee of record of the entire interest. See 37 CFR 3.71.						
Assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)						
SIGNATURE of Applicant or Assignee of Record						
Signature // TR	Col			Date	28 JANOS	
Name Timothy R. Cr	oll			Telephone	(408) 433-7625	
Title and Company Senior Corporate Counsel, IP						
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.						
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This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentially is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, land submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Dispartment of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTC/SB/96 (09-04)

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	STATEMENT UNDER 37 CFR 3.73(b)					
Applicant/Patent Owner: LSI Logic Corporation						
Application No./Patent No.: 10/659	134 Filed/Issue Date: September 10	, 2003				
Entitled: Apparatus and Method of Ma Carbide	anufacture for Integrated Circuit and CMOS Device Includ	ding Epitaxally Grown Dielectric on Silicon				
LSI Logic Corporation (Name of Assignee)	, a <u>Corporation</u> (Type of Assignee, e.g., corporation	n, partnorship, university, government agency, etc.)				
states that it is:  1.   the assignee of the entire right, title, and interest; or						
2. an assignee of less than the entire right, title and interest.  The extent (by percentage) of its ownership interest is%						
in the patent application/patent identified above by virtue of either:						
A assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel <u>014497</u> , Frame <u>0375</u> , or for which a copy thereof is attached.						
B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:						
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Additional documents in the chain of title are listed on a supplemental sheet.  Copies of assignments or other documents in the chain of title are attached.  [NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]						
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.						
	Signature	Date				
	Timothy R. Croli	(408) 433-7625				
Print	ted or Typed Name	Telephone Number				
Senior	Corporate Counsel, IP Title					

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